

Applicant: Hofmeister, et al.  
 Serial No.: 10/695,342  
 Filing Date: October 28, 2003  
 For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT  
 AND METHOD FOR FIBER OPTICS DEVICE

Att'y Docket No.: 15436.253.66.1  
 Group: 2828



INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

U.S. Patent Documents

Examiner Initial*	Document Number	Issue Date	Name
W 1	4,359,553	11/16/1982	Edwards
2	4,378,451	03/29/1983	Edwards
3	4,687,924	08/18/1987	Galvin et al.
4	4,734,914	03/29/1988	Yoshikawa
5	4,747,091	05/24/1988	Doi
6	4,809,286	02/28/1989	Kollanyi et al.
7	4,916,707	04/10/1990	Rosenkranz
8	4,932,038	06/05/1990	Windus
9	5,019,769	05/28/1991	Levinson
10	5,039,194	08/13/1991	Block et al.
11	5,041,491	08/20/1991	Turke et al.
12	5,268,949	12/07/1993	Watanabe et al.
13	5,287,375	02/1994	Fujimoto
14	5,334,826	08/02/1994	Sato et al.
15	5,383,208	01/17/1995	Queniat et al.

Examiner:

Date Considered:

11/8/07

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Hofmeister, et al.

Serial No.: 10/695,342

Att'y Docket No.: 15436.253.66.1

Filing Date: October 28, 2003

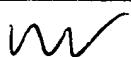
Group: 2828

For:

TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT  
AND METHOD FOR FIBER OPTICS DEVICE

<u>16</u>	5,392,273	02/21/1995	Masaki et al.
<u>17</u>	5,396,059	03/07/1995	Yeates
<u>18</u>	5,448,629	09/05/1995	Bosch et al.
<u>19</u>	5,516,563	05/14/1996	Schumann et al.
<u>20</u>	5,557,437	09/17/1996	Sakai et al.
<u>21</u>	5,574,435	11/12/1996	Mochizuki
<u>22</u>	5,594,748	01/14/1997	Jabr
<u>23</u>	5,604,758	02/1997	AuYeung et al.
<u>24</u>	5,673,282	09/30/1997	Wurst
<u>25</u>	5,748,672	05/1998	Smith et al.
<u>26</u>	5,761,216	06/02/1998	Sotome et al.
<u>27</u>	5,801,866	09/01/1998	Chan et al.
<u>28</u>	5,812,572	09/22/1998	King et al.
<u>29</u>	5,854,704	12/29/1998	Grandpierre
<u>30</u>	5,926,303	07/20/1999	Giebel et al.
<u>31</u>	5,953,690	09/14/1999	Lemon et al.
<u>32</u>	5,956,168	09/21/1999	Levinson et al.
<u>33</u>	5,966,395	10/1999	Ikeda
<u>34</u>	6,055,252	04/2000	Zhang

Examiner:



Date Considered:

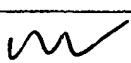
1/17/07

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Hofmeister, et al.  
Serial No.: 10/695,342  
Filing Date: October 28, 2003  
For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT  
AND METHOD FOR FIBER OPTICS DEVICE

Att'y Docket No.: 15436.253.66.1  
Group: 2828

35	6,064,501	05/16/2000	Roberts et al.
36	6,157,022	12/05/2000	Meada et al.
37	6,160,647	12/12/2000	Gilliland et al.
38	6,175,434	01/2001	Feng
39	6,188,059	02/13/2001	Nishiyama et al.
40	6,198,558	03/2001	Graves et al.
41	6,205,505	03/20/2001	Jau et al.
42	6,222,660	04/24/2001	Traa
43	6,229,788	05/2001	Graves et al.
44	6,256,127	07/03/2001	Taylor
45	6,292,497	09/2001	Nakano
46	6,313,459	11/2001	Hoffe et al.
47	6,423,963	07/23/2002	Wu
48	6,473,224	10/29/2002	Dugan et al.
49	6,512,617	01/28/2003	Tanji et al.
50	6,519,255	02/2003	Graves
51	6,526,076	02/2003	Cham et al.
52	6,570,149	05/2003	Maruyama et al.
53	6,594,050	07/2003	Jansson et al.

Examiner:  Date Considered: 11/19/07

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Hofmeister, et al.  
 Serial No.: 10/695,342  
 Filing Date: October 28, 2003  
 For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT  
 AND METHOD FOR FIBER OPTICS DEVICE

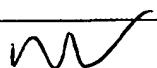
Att'y Docket No.: 15436.253.66.1  
Group: 2828

<u>M</u> 54	6,631,146	10/07/2003	Pontis et al.
<u>      </u> 55	6,643,472	11/2003	Sakamoto et al.
<u>      </u> 56	6,661,836	12/2003	Dalal et al.
<u>      </u> 57	6,694,462	02/2004	Reis et al.
<u>      </u> 58	6,748,181	06/2004	Miki et al.
<u>      </u> 59	6,937,949	08/30/2005	Fishman et al.
<u>      </u> 60	6,941,077	09/06/2005	Aronson et al.
<u>      </u> 61	6,952,531	10/04/2005	Aronson et al.
<u>      </u> 62	7,020,567	03/28/2006	Fishman et al.
<u>V</u> 63	7,058,310	06/06/2006	Aronson et al.

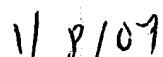
U.S. Published Patent Application Documents

Examiner <u>Initial</u> * <u>M</u>	Document <u>Number</u>	Pub. <u>Date</u>	Name
<u>      </u> 64	2001/0046242	11/29/2001	Kawakami et al.
<u>      </u> 65	2001/0046243	11/29/2001	Schie
<u>      </u> 66	2002/0021468	02/21/2002	Kato et al.
<u>      </u> 67	2002/0027688	03/07/2002	Stephenson
<u>V</u> 68	2002/0060824	05/23/2002	Liou et al.

Examiner:



Date Considered:



\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Hofmeister, et al.  
Serial No.: 10/695,342  
Filing Date: October 28, 2003  
For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT  
AND METHOD FOR FIBER OPTICS DEVICE

At'ty Docket No.: 15436.253.66.1  
Group: 2828

69	2002/0097468	07/25/2002	Mecherle et al.
70	2002/0101641	08/2002	Kurchuk
71	2002/0105982	08/2002	Chin et al.
72	2002/0129379	09/2002	Levinson et al.
73	2002/0149821	10/17/2002	Aronson et al.
74	2002/0181519	12/2002	Vilhelmsson et al.
75	2002/0181894	12/05/2002	Gilliand et al.
76	2003/0053170	03/2003	Levinson et al.
77	2003/0110509	06/2003	Levinson et al.
78	2003/0113118	06/19/2003	Bartur
79	2003/0169790	09/11/2003	Chieng et al.
80	2003/0210917	11/13/2003	Stewart et al.
81	2004/0076113	04/22/2004	Aronson et al.
82	2004/0120720	06/24/2004	Chang et al.
83	2004/0153913	08/05/2004	Fishman et al.
84	2004/0202210	10/14/2004	Thornton
85	2004/0240886	12/02/2004	Aronson et al.
86	2004/0253003	12/2004	Farmer et al.
87	2005/0031352	02/10/2005	Light et al.

Examiner:

WW

Date Considered:

1/1/07

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Hofmeister, et al.  
 Serial No.: 10/695,342  
 Filing Date: October 28, 2003  
 For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT  
 AND METHOD FOR FIBER OPTICS DEVICE

Atty Docket No.: 15436.253.66.1  
 Group: 2828

DU 88 2005/0058455 03/17/2005 Hosking et al.

Foreign Patent Documents

Examiner <u>Initial*</u>	Document <u>Number</u>	Publication <u>Date</u>	Country or <u>Patent Office</u>
<u>DU</u> 89	JP 402102589 A	04/16/1990	Japan
<u>      </u> 90	JP 404023373 A	01/1992	Japan
<u>      </u> 91	06209209 A	07/26/1994	Japan (Abstract)
<u>      </u> 92	09162811 A	06/20/1997	Japan (Abstract)
<u>      </u> 93	WO 98/00893	01/08/1998	PCT
<u>      </u> 94	WO 98/00943	08/01/1998	PCT
<u>      </u> 95	EP0745868B1	04/17/2002	EPO
<u>      </u> 96	PCT/US02/03226	05/09/2002	PCT (search report)
<u>      </u> 97	WO 02/063800 A1	08/15/2002	PCT
<u>      </u> 98	EP 02704344	10/05/2004	EPO (search report)
<u>      </u> 99	EP 04017254	10/05/2004	EPO (search report)
<u>      </u> 100	WO/2004/098100	11/11/2004	PCT
<u>      </u> 101	PCT/US04/11130	10/12/2004	Search Report
<u>      </u> 102	EP 1471671 A2	12/27/2004	EPO

Examiner: W Date Considered: 1/8/07

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Hofmeister, et al.  
Serial No.: 10/695,342  
Filing Date: October 28, 2003  
For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT  
AND METHOD FOR FIBER OPTICS DEVICE

Att'y Docket No.: 15436.253.66.1  
Group: 2828

<u>W</u>	103	JP 58140175 A (abstract)	08/19/1983	Japan
	104	JP 62124576 A (abstract)	06/05/1987	Japan
	105	JP 62235975 A (abstract)	10/16/1987	Japan
	106	JP 62281485 A (abstract)	12/07/1987	Japan

Other Documents  
(including author, title, pertinent pages, etc.)

Examiner  
Initial\*

W

- 107 Yi Cai et al., "Jitter testing for gigabit serial communication transceivers," Jan – Feb 2002, IEEE Design and Test of Computers, Vol. 19, Issue 1, pp 66-74.
- 108 MAEDA, Noriyuki "Notification of Reason(s) for Refusal," Japanese Patent Application No. JP2002-563630, Nakamura, M. et al., July 13, 2005.
- 109 Finisar Corp., "App. Note AN-2025: Using the Finisar GBIC I<sup>2</sup>C Test Diagnostics Port," 1998.
- 110 Hausdorf, Reiner, "Mobile Transceiver Measurements with Radiocommunication Service Monitor CMS," News from Rohde & Schwarz, 127, IV, 1989, pp 4-7.
- 111 Webopedia: The 7 Layers of the OSI Model [online] [retrieved 10/15/03]. Retrieved from Internet: URL: [http://webopedia.internet.com/quick\\_ref/OSI\\_Layers.asp](http://webopedia.internet.com/quick_ref/OSI_Layers.asp)
- 112 Webopedia.com: Public-Key Encryption [online] [retrieved 10/15/03]. Retrieved from Internet: URL: [http://www.webopedia.com/TERM/p/public\\_key\\_cryptography.html](http://www.webopedia.com/TERM/p/public_key_cryptography.html)

Examiner: W Date Considered: 11/10/07

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Hofmeister, et al.  
Serial No.: 10/695,342  
Filing Date: October 28, 2003  
For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT  
AND METHOD FOR FIBER OPTICS DEVICE

At'ty Docket No.: 15436.253.66.1  
Group: 2828

DV 113 Webopedia.com: MAC Address [online] [retrieved 10/15/03]. Retrieved from Internet: URL: [http://www.webopedia.com/TERM/M/MAC\\_address.html](http://www.webopedia.com/TERM/M/MAC_address.html)

114 Webopedia.com: 12C [online] [retrieved 11/11/03]. Retrieved from Internet: URL: <http://www.webopedia.com/TERM/I/12C.html>

115 Manchester Encoding [online] [retrieved 11/12/03]. Retrieved from Internet: URL: <http://www.erg.abdn.ac.uk/users/gorry/course/phy-pages/man.html>

116 Documentation entitled "IR Receiver ASSP: T2525", copyright 2003 by Atmel Corporation

117 Documentation entitled "IR Receiver for Data Communication: U2538B", copyright 2003 by Atmel Corporation

118 Documentation entitled "Low-Voltage Highly Selective IR Receiver IC: T2527", copyright 2002 by Atmel Corporation

119 Documentation entitled "Application Note: T2525/26/27", copyright 2003 by Atmel Corporation

120 *LXT16706/16707 SerDes Chipset*, Intel Products,  
[www.intel.com/design/network/products/optical/phys/lxt16706.htm](http://www.intel.com/design/network/products/optical/phys/lxt16706.htm),  
April 19, 2002.

121 *LXT35401 XAUI-to-Quad 3.2G Transceiver*, Intel Products,  
[www.intel.com/design/network/products/optical/phys/lxt35401.htm](http://www.intel.com/design/network/products/optical/phys/lxt35401.htm),  
April 19, 2002

122 Texas Instruments User's Guide, *TLK2201 Serdes EVM Kit Setup and Usage*,  
Mixed Signal DSP Solutions, July 2000.

123 Texas Instruments User's Guide, *TLK1501 Serdes EVM Kit Setup and Usage*,  
Mixed Signal Products, June 2000.

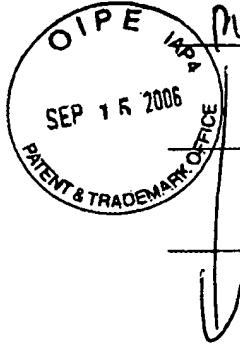
Examiner:

Date Considered:

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Hofmeister, et al.  
 Serial No.: 10/695,342  
 Filing Date: October 28, 2003  
 For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT  
 AND METHOD FOR FIBER OPTICS DEVICE

Att'y Docket No.: 15436.253.66.1  
 Group: 2828



124 National Semiconductor DS92LV16 Design Guide, *Serializing Made Simple*, February 2002.

125 Vaishali Semiconductor, *Fibre Channel Transceiver*, VN16117, MDSN-0002-02, 08/09/2001.

126 Fairchild Semiconductor, Application Note 77, *CMOS, the Ideal Logic Family*, January 1983.

127 Analog Target Specification, Annex 48B, Published by IEEE New York, 05/2001, pp. 6-14.

#### References Cited by Applicants

While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

Examiners will consider all citations submitted in conformance with 37 C.F.R. § 1.98 and MPEP Sec. 609 and place their initials adjacent the citations in the spaces provided on this form. Examiners will also initial citations not in conformance with the guidelines which may have been considered. A reference may be considered by the Examiner for any reason whether or not the citation is in full conformance with the guidelines. A line will be drawn through a citation if it is not in conformance with the guidelines AND has not been considered. A copy of the submitted form, as reviewed by the Examiner, will be returned to the applicant with the next communication. The original of the form will be entered into the application file.

Each citation initialed by the Examiner will be printed on the issued patent in the same manner as references cited by the Examiner on Form PTO-892.

The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

GPM0000002495V001

Examiner:

Date Considered:

1/19/07

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.